

FIG. 1 is a block diagram of a system 8 for processing video data. The system 8 includes a video source 30, a video input device 18, a processor 12, a memory structure 14, a local database 22, an external database 24, a user input device 19, an output device 20, and a user 40. The video source 30 is connected to the video input device 18 via a line 25. The video input device 18 is connected to the processor 12 via a line 18. The processor 12 is connected to the memory structure 14 via a line 14. The memory structure 14 contains computer code 32. The processor 12 is also connected to the local database 22 via a line 22. The processor 12 is connected to the external database 24 via a line 26. The processor 12 is connected to the user input device 19 via a line 19. The user input device 19 is connected to the user 40. The processor 12 is connected to the output device 20 via a line 20. The output device 20 is connected to the user 40.

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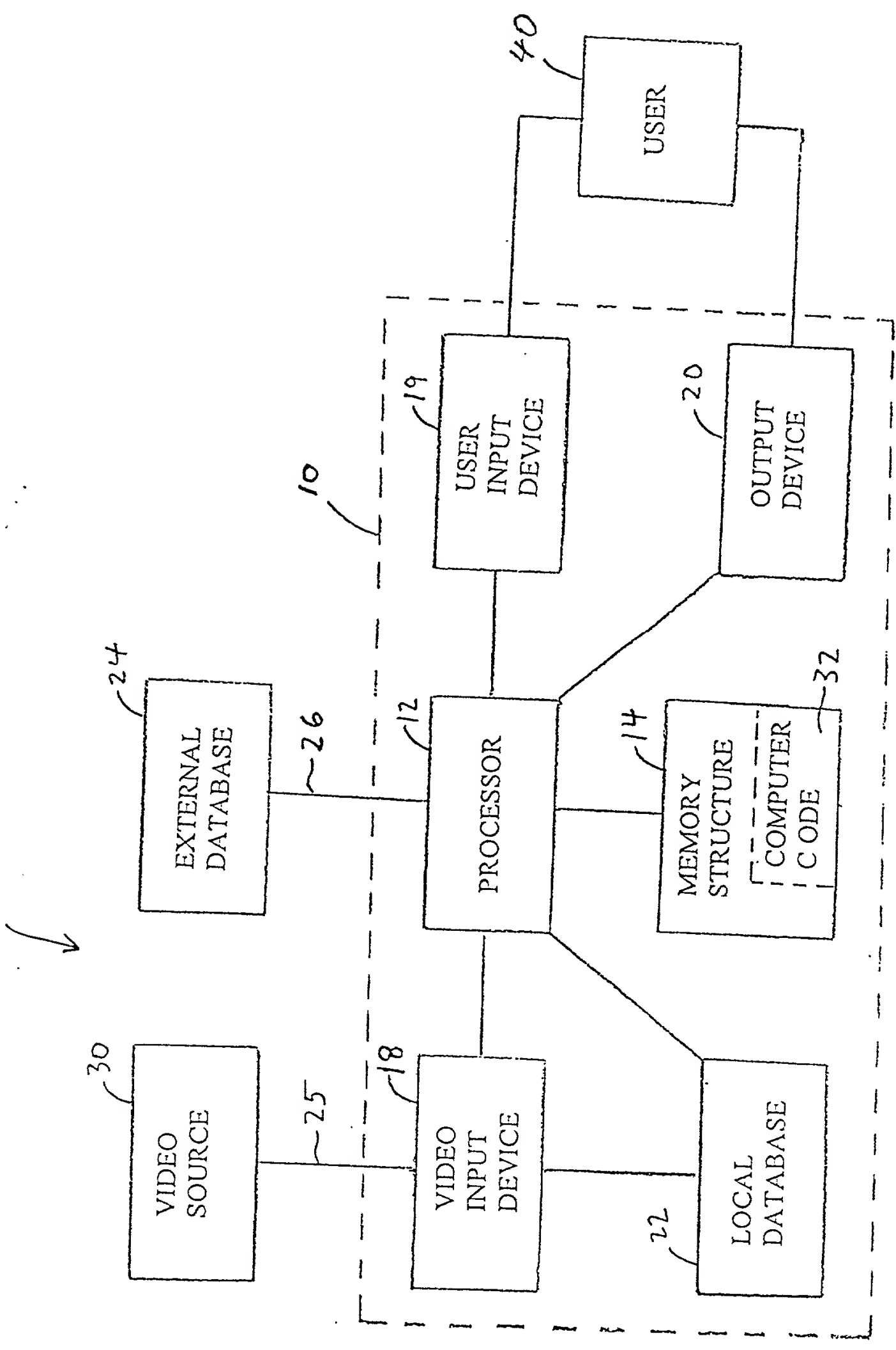


FIG. 1

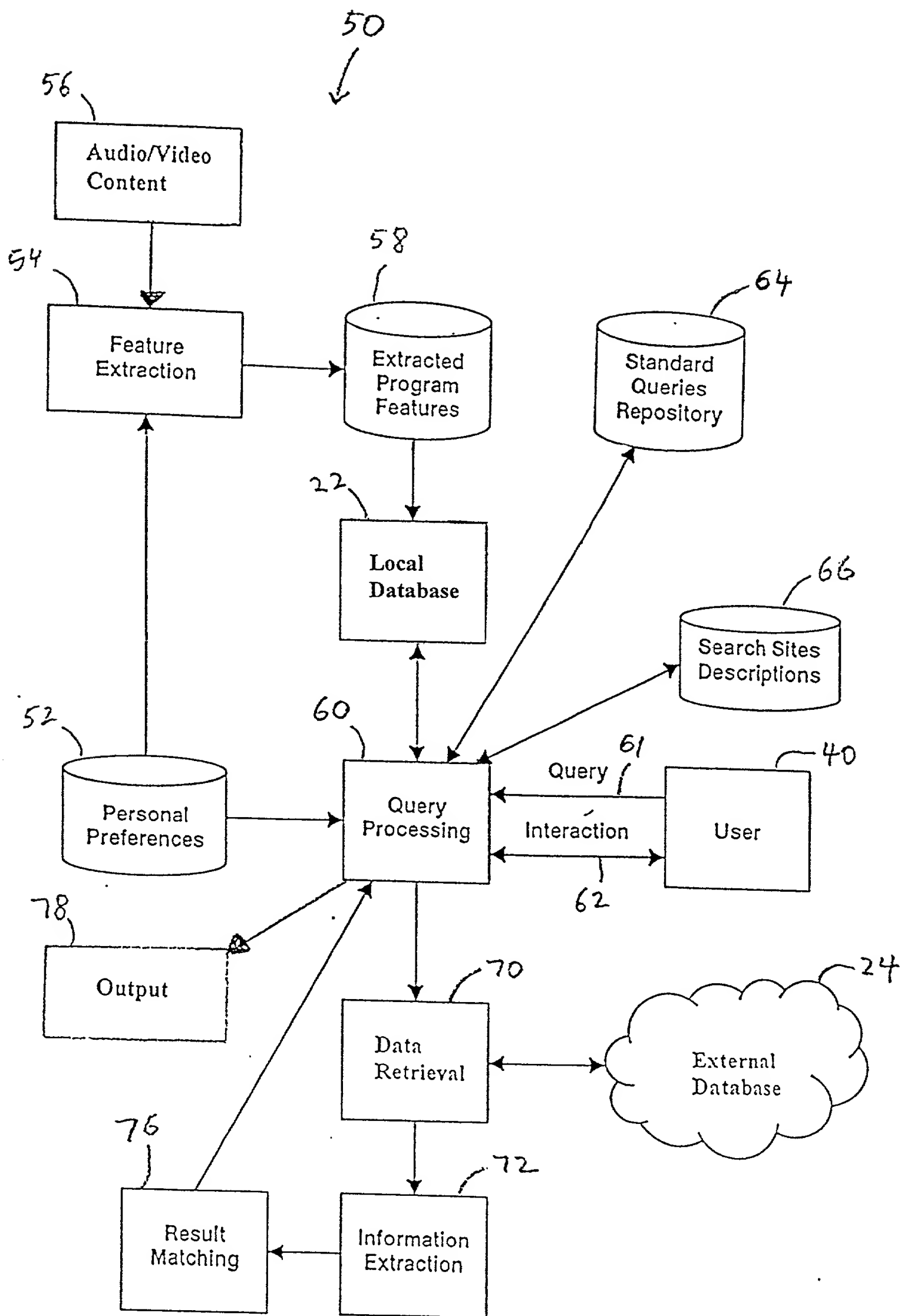


FIG. 2